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AF/2188  
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Docket No.: 57454-132

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Akira YAMADA

RESPONSE UNDER 37 CFR 1.116  
**EXPEDITED PROCEDURE**

Appl. No.: 09/874,239

Customer No.: 20277

Filed: June 06, 2001

Confirmation No.: 5917

Group Art Unit: 2188

Examiner: Thang H. Ho

Title: SYNCHRONOUS SIGNAL PRODUCING CIRCUIT FOR CONTROLLING A DATA  
READY SIGNAL INDICATIVE OF END ACCESS TO A SHARED MEMORY AND  
THEREBY CONTROLLING SYNCHRONIZATION BETWEEN PROCESSOR AND  
COPROCESSOR

**REQUEST FOR RECONSIDERATION**

**RECEIVED**  
SEP 24 2004  
Technology Center 2000

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Reconsideration of the rejection imposed in the Office Action dated June 23, 2004 is  
solicited in light of the following Remarks.